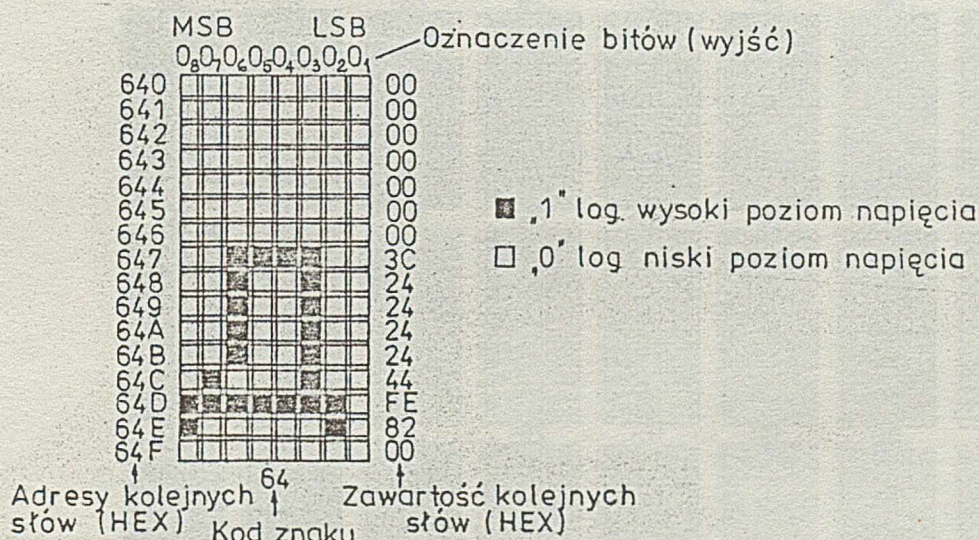






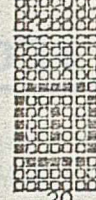
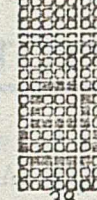
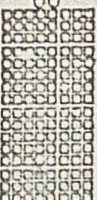





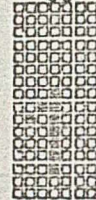
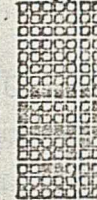























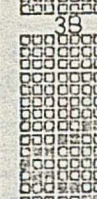







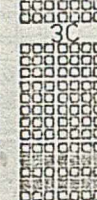
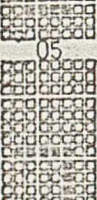




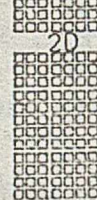

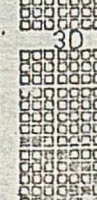
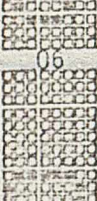


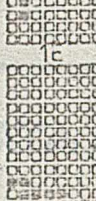
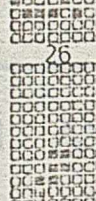
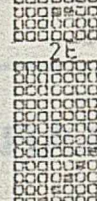





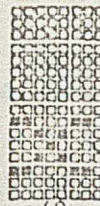

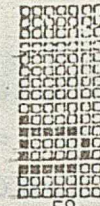

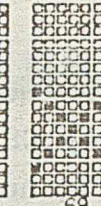
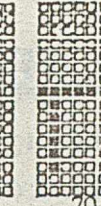
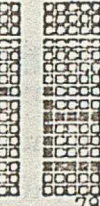



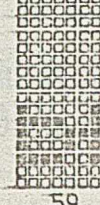
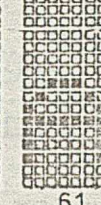
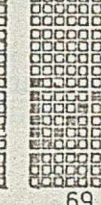
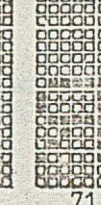
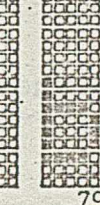



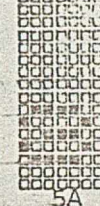

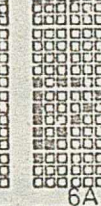






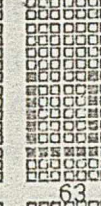





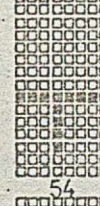
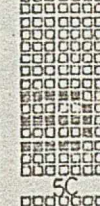









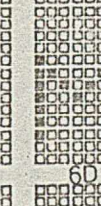








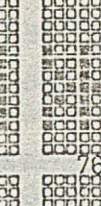











Układ MCY 7316N AJ jest wersją pamięci ROM 16K MCY 7316 o zawartości odpowiadającej generatorowi znaków alfanumerycznych. W pamięci zaprogramowano 128 znaków /znaki kodu ASCII, duże i małe litery rosyjskie/. Typowe zastosowanie: monitory ekranowe. Każdy znak zapisany jest w matrycy 9 x 7 przy wykorzystaniu 16-tu słów 8-mio bitowych. Dany znak tworzą słowa w układzie poziomym wybierane kolejnymi adresami; omiatanie najmłodszyimi bitami adresowymi A₃, A₂, A₁, A₀ od góry do dołu /rys. 1/. Znaczącemu /świejącemu/ polu znaku odpowiada "1" logiczna - wysoki poziom napięcia /zaciemnione pola na rysunku/, pozostałym - "0" logiczna /pola niezaciemnione/.



Rys. 1. Przykładowy zapis znaku "Д"

| | | | | | | | |
|---|---|---|---|---|---|---|---|
|  |  |  |  |  |  |  |  |
| 00 | 08 | 10 | 18 | 20 | 28 | 30 | 38 |
|  |  |  |  |  |  |  |  |
| 01 | 09 | 11 | 19 | 21 | 29 | 31 | 39 |
|  |  |  |  |  |  |  |  |
| 02 | 0A | 12 | 1A | 22 | 2A | 32 | 3A |
|  |  |  |  |  |  |  |  |
| 03 | 0B | 13 | 1B | 23 | 2B | 33 | 3B |
|  |  |  |  |  |  |  |  |
| 04 | 0C | 14 | 1C | 24 | 2C | 34 | 3C |
|  |  |  |  |  |  |  |  |
| 05 | 0D | 15 | 1D | 25 | 2D | 35 | 3D |
|  |  |  |  |  |  |  |  |
| 06 | 0E | 16 | 1E | 26 | 2E | 36 | 3E |
|  |  |  |  |  |  |  |  |
| 07 | 0F | 17 | 1F | 27 | 2F | 37 | 3F |

| | | | | | | | |
|---|---|---|---|---|---|--|---|
|  40 |  48 |  50 |  58 |  60 |  68 |  70 |  78 |
|  41 |  49 |  51 |  59 |  61 |  69 |  71 |  79 |
|  42 |  4A |  52 |  5A |  62 |  6A |  72 |  7A |
|  45 |  48 |  53 |  58 |  63 |  68 |  73 |  78 |
|  44 |  4C |  54 |  5C |  64 |  6C |  74 |  7C |
|  45 |  4D |  55 |  5D |  65 |  6D |  75 |  7D |
|  46 |  4E |  56 |  5E |  66 |  6E |  76 |  7E |
|  47 |  4F |  57 |  5F |  67 |  6F |  77 |  7F |

Wejścia "wyboru modułu" dla tej pamięci zaprogramowano w sposób następujący CS1 = 0, CS2 = 0, CS3 = 1.

Zestaw znaków generatora znaków MCY 7316N AJ przedstawiono obok.

Dokumenty związane: TWT-82/CEMI-B10/B20. Cyfrowe mikroukłady scalone MOS. Pamięci stałe ROM MCY 7316N XX.

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